

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Mok, et al.

Serial No.: 09/785,815

Confirmation No.: 4127

Filed: February 16, 2001

For: Integrated Semiconductor
Substrate Bevel Cleaning
Apparatus and Method

Group Art Unit: 1763

Examiner: Culbert, Roberts P.

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING
37 CFR 1.8

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12/8/04 Re: 42262
Date Signature

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The Applicants, and the Attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

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The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

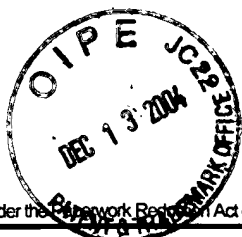
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Respectfully submitted,



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**SUPPLEMENTAL
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Sheet 1 of 1

Complete if Known

Application Number	09/785,815
Filing Date	FEBRUARY 16, 2001
First Named Inventor	MOK, ET AL.
Art Unit	1763
Examiner Name	CULBERT, ROBERTS P.
Attorney Docket Number	AMAT/2601.P11/PPC/ECP/RKK

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C1	PCT International Written Opinion for PCT/US01/42412, dated August 5, 2004 (AMAT/4969.PC)	

Examiner
SignatureDate
Considered

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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